

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re application of:** Agarwal et al.

**Application No.** 09/590,795

**Filed:** June 8, 2000

**Confirmation No.** 9849

**For:** METHODS FOR FORMING AND  
INTEGRATED CIRCUIT STRUCTURES  
CONTAINING RUTHENIUM AND  
TUNGSTEN CONTAINING LAYERS

**Examiner:** Cuong Q. Nguyen

**Art Unit:** 2811

**Attorney Reference No.** 6047-53173

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Attorney  
for Applicant(s)



**INFORMATION DISCLOSURE STATEMENT  
PURSUANT TO 37 C.F.R. § 1.97(b)(3)**

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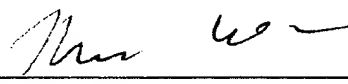
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Applicants are filing this Information Disclosure Statement ("IDS") before the mailing date of a first Office action on the merits. As a result, no fee should be required to file this IDS. However, if the Patent Office determines that a fee is required for Applicants to file this IDS, please charge any such fees, or credit overpayment, to Deposit Account No. 02-4550. A **duplicate** copy of this Information Disclosure Statement is enclosed.

The filing of this IDS shall not be construed to be an admission that the information cited in the statement is, or is considered to be, prior art or otherwise material to patentability as defined in 37 C.F.R. §1.56.

Respectfully submitted,

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<p>* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>	